

S/N 10/010,737

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Atila Alvandpour et al.

Examiner: Anh Q. Tran

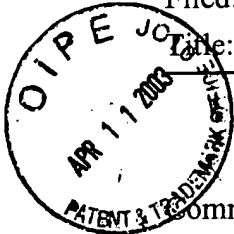
Serial No.: 10/010,737

Group Art Unit: 2819

Filed: December 7, 2001

Docket: 884.451US1

Title: VOLTAGE-LEVEL CONVERTER



AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents  
Washington, D.C. 20231

Applicant has reviewed the office action mailed on October 2, 2002.

This response is accompanied by a petition, as well as the appropriate fee, to obtain a three-month extension of the period of time for responding to the office action, thereby moving the deadline for response from January 2, 2003 to April 2, 2003.

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IN THE CLAIMS

There are no amendments to the claims.

1. (Original) An logic unit comprising:  
a first logic unit connected to a first supply voltage;  
a second logic unit connected to a second supply voltage; and  
a voltage-level converter connecting the first logic unit to the second logic unit, the voltage-level converter including at least one transistor having a threshold voltage greater than or about equal to the difference between the second supply voltage and the first supply voltage and the at least one transistor connected to the second supply voltage.
2. (Original) The logic unit of claim 1, wherein the first logic unit comprises a memory unit.
3. (Original) The logic unit of claim 2, wherein the second logic unit comprises an arithmetic unit.